

# Faculty CV

1. Name of Faculty: BHANGALE TEJESHREE JAGDISH
2. Present Position : Lecturer in Electrical Engineering
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6. Date of Birth : 5 th April 1988
7. Unique ID : DTETJBF8801

## QUALIFICATIONS:

Sr. No.	Examination	Institute Name	Board/ University	% of Marks	Class Award
1	B. E.	Government Engineering College,Aurangabad	Dr B.A.M.U	68%	First Class with Distinction
2	M. E.	Government Engineering College,Aurangabad	Dr B.A.M.U	CGPA-7.65	First Class with Distinction

## INDUSTRIAL EXPERIENCE: NIL

## TEACHING CAREER DETAILS:

Sr. No.	Organization	Designation	Duration	Period	Work Done in Brief
1	Government Polytechnic, Awasari	Lecturer in Electrical Engineering	2/11/2016 to 31/8/2024	8 years	Teaching ,NBA , MSBTE Activities
2.	Government Polytechnic, Pune	Lecturer in Electrical Engineering	1/9/2024 to till date		Teaching ,NBA , MSBTE Activities

**COURSES TAUGHT AT DIPLOMA/ GRADUATION LEVEL:** - Switchgear and Protection, Basic Electrical Engineering, Management, Digital Electronics etc.

**Projects Undertaken and Taken out:** - NIL

**Papers Published in Journal :-** Analysis of PI and Fuzzy logic Controller for Single Phase Induction Motor (IJITEE- 2020)